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Application Number	10/773,220
Filing Date	February 9, 2004
First Named Inventor	Shin TAKANEZAWA
Art Unit	1712
Examiner Name	Robert E. Sellers
Attorney Docket Number	TSUK0008

[illegible]

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>2</sup> *Number <sup>4</sup> *Kind Code <sup>5</sup> (if known)				
	B	EP 1 103 575 A1	05-30-2001	Matsushita		
	C	DE 699 07 123 T2	04-01-2004	NEC Corp.		
	D	EP 1 013 684 B1	04-23-2003	NEC Corp.		
	E	DE 698 15 601 T2	04-29-2004	Ajinomoto Co.		
	F	EP 0 893 474 B1	06-18-2003	Ajinomoto Co.		

Date Considered	10/31/2005
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### Complete if Known

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Sheet 2 of 2

### NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	G	<del>COOMBS, CLYDE F. JR., Printed Circuits Handbook, 1988, pages 4.11 4.12, 6.3 6.5, 3rd Edition, McGraw-Hill</del>	
	H	<del>Translation of Office Action dated April 22, 2005 in corresponding German application DE 10 2004 006 312.5</del>	✓

Examiner Signature	ROBERT SELLERS	Date Considered	10/31/2005
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